

RESUME

CHELLA JYOTHIRMAYI

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CAREER OBJECTIVE:

To secure a challenging position where I can effectively contribute my skills, and innovative ideas to prove myself in a work-oriented environment.

EDUCATIONAL QUALIFICATION:

- **M.TECH* (VLSI)** with **aggregate 71%** in 2016-2018 from St.Mary's Women's Engineering College, Budampadu, Guntur.
- **B.TECH*(Electronics & Communication Engineering)** with **aggregate 77.05%** in 2010-2014 from KKR & KSR Institute of Technology & Sciences (JNTUK), Guntur.
- **12th** with **aggregate 79.1%** from Sri Chaitanya Junior College, Guntur in 2010.
- **10th** with **aggregate 71.0%** from Rosette English Medium School, Guntur in 2008.

TECHNICALSKILLS:

- Programming Languages : C, VHDL, Verilog, MP&MC.
- Operating System : Windows XP, Windows 7, Windows 8, windows10.
- Simulation Software : MATLAB, KICAD, Xilinx, Xilinx Vivado, MASM, Keil, Circuit Wizard, Arduino, Easy EDA.

MAIN PROJECT WORK:

UG:

- PROJECT TITLE : Vehicle Tracking and Accident Detection System. (Embedded Systems).
- TEAM SIZE : 4
- DESCRIPTION : The main intention of this project is to detect the accident spot with significantly less time and sends the basic information to first aid center Within a few seconds through GPS & GSM networks.

PG:

- PROJECT TITLE : Implementation of Efficient Constant Multiplier Architecture based On VHBCSE Algorithm for reconfigurable FIR Filter.

CURRICULARACTIVITIES:

- Participated in paper presentation on "blue eyes" at the KKR&KSR Institute of Technology & Sciences.
- Participated in the PCB DESIGN workshop conducted by Indo Global Services at KKR&KSR Institute of Technology & Sciences, Guntur held on 22nd&23rd Of July 2013.
- Participated in AICTE Training And Learning (ATAL) Academy Online FDP on "Internet of Things (IoT)" from 07-05-2020 to 11-05-2020 at Indian Institute of Information Technology(IIT Nagpur).
- Participated in One Week Online FDP on "Recent Trends and Research Areas in Applied VLSI and Advanced Communications" from 08th June to 12th June 2020.

- Participated in AICTE Training And Learning (ATAL) Academy Online FDP on "Robotics" from 11-06-2020 to 15-06-2020 at COLLEGE OF TECHNOLOGY AND ENGINEERING, UDAIPUR.

PAPER PUBLICATIONS:

- Published a paper in International Journal of Innovative Technology and Exploring Engineering (IJITEE) on “**Design of various low power and High speed Full Adder Designs using 45nm Cmos technology**” in Volume-11 Issue-5, April 2022.
- Published a paper in International Journal of Innovative Technology and Exploring Engineering (IJITEE) on “**Design of a 4 bit Arithmetic and Logical unit with Low Power and High Speed**” in Volume-10 Issue-5, March 2021.
- Published a paper in Kala Sarovar Journal (UGC Listed journal) on “**An Exploring Low Power and Fast Full Adder Using XOR/XNOR Gates with Adiabatic Technology**” in Vol-23 No.02(V) July-September 2020.
- Published a paper in International Journal for Advanced Research in Science & Technology (IJARST) on “**Implementation of Efficient Constant Multiplier Architecture Based on VHBCSE Algorithm for Reconfigurable Fir Filter**” in Volume 08, Issue 03, June 2018.

PERSONAL SKILLS:

- Acceptance of Responsibility.
- Able to grasp new things quickly.
- Always owed with “can-do spirit”.

EXPERIENCE:

- Worked as a teacher and computer operator (Administration) in OXFORD PUBLIC SCHOOL.
- Working as an Asst. Professor at Hindu College of Engineering and Technology since 2017 December.

STRENGTHS:

- Hard-working and friendly.
- Easily adjustable to new environments
- Disciplined and honest.

PERSONAL PROFILE:

Father's Name	: Ch. Siva Ramaiah
Mother's Name	: Ch.Udaya Kumari
Date of Birth	: 24-06- 1993
Nationality	: Indian
Gender	: Female
Hobbies	: Playing indoor games.
Languages Known	: English, Telugu.
Contact Information	: D: No: 26-33-7, A.T.Agraharam, 7 th line,Guntur, A.P, 522004.

DECLARATION:

I hear by declaring that the above-mentioned information is correct to the best of my knowledge.

PLACE: Guntur,

DATE:

Ch. Jyothirmai.